AMENDMENT TO THE CLAIMS

1. (Currently Amended) A system for providing digital entertainment data, the system comprising:

a shared system bus ~~plurality of buses~~ interconnecting internal components of a gateway, the gateway comprising:

a processor having a processor input connected to [[a]] the shared system [[data]] bus ~~of the plurality of buses~~ and a processor output connected to the shared system [[data]] bus;

memory having a memory input connected to the shared system [[data]] bus and a memory output connected to the shared system [[data]] bus ;

multiple pairs of a tuner and a demodulator connected to the shared system bus to receive control signals, with each pair of the tuner and the demodulator tuning to receive ~~receiving~~ a plurality of information signals from a source;

a data switch having multiple input ports and multiple output ports;

a dedicated link between each one of the multiple input ports and a respective one of the multiple pairs of the tuner and the demodulator, such that each pair of the multiple pairs of the tuner and the demodulator is dedicated to a different input port of the data switch; and

~~a media bus of the plurality of buses having a first media bus input connected to the multiple pairs of the tuner and the demodulator that receives the plurality of information signals from the multiple pairs of the tuner and the demodulator, the media bus having three media bus outputs, with a first media bus output connected to the system data bus;~~

a video overlay processor coupled to the data switch that superimposes ~~having three video overlay processor inputs and a video overlay processor output, a first video overlay processor input connected to a second media bus output of the media bus, a~~

~~second video overlay processor input connected to a third media bus output of the media bus, and a third video overlay processor input connected to the system data bus, the video overlay processor output connected to the system data bus, the video overlay processor receiving the plurality of information signals from the media bus and superimposing~~ a first audio-visual signal over a second audio-visual signal to produce a superimposed signal ~~and outputting the superimposed signal over the video overlay processor output to the system data bus~~;

~~a network bus of the plurality of buses having a network bus input connected to the system data bus and receiving the superimposed signal; and~~

~~a data switch connected to the multiple pairs of a tuner and a demodulator, the data switch having multiple input ports with each pair of the multiple pairs of tuner and demodulator connected to a respective dedicated one of the multiple input ports, such that each pair of the multiple pairs of tuner and demodulator is dedicated to a different input port of the data switch, the data switch also having an input port connected to the network bus that receives the superimposed signal and that sends the superimposed signal to an output switch port~~.

2. (Cancel)
3. (Cancel)
4. (Cancel)

5. (Currently Amended) The system of claim 1, further comprising a mass storage device connected to the shared system [[data]] bus that stores an item identifier corresponding to a content item stored in the mass storage device, the item identifier having a first data field that indicates the content item has been played, a second data field indicating the content item has been purchased, and a third data field indicating the content item has been licensed.

6. (Currently Amended) The system of claim 1, further comprising a mass storage device connected to the shared system [[data]] bus that stores an item identifier corresponding to

a content item stored in the mass storage device, the item identifier storing a cost of playback for the content item and a second cost of purchase for the content item.

7. (Previously Presented) The system of claim 1, further comprising:

decryption logic having an input connected to the multiple pairs of the tuner and the demodulator; and

a card reader having a card reader input and a card reader output, the card reader input connected to an output of the decryption logic, the card reader providing authorization for the decryption logic to decrypt the plurality of information signals to produce decrypted digital information.

8. (Previously Presented) The system of claim 1, further comprising a card reader that receives authorization to decrypt encrypted digital information received from the multiple pairs.

9. (Currently Amended) The system of claim 8, further comprising decoder logic connected to the shared system bus ~~media bus~~.

10. (Previously Presented) The system of claim 1, wherein the plurality of information signals include a plurality of television program signals.

11. (Previously Presented) The system of claim 1, wherein the plurality of information signals include an audio signal.

12. (Previously Presented) The system of claim 1, wherein the plurality of information signals include a data signal.

13. (Previously Presented) The system of claim 1, wherein the plurality of information signals are received from a transmission facility selected from the group consisting of a direct broadcast satellite, a cable headend, and a terrestrial transmitter.

14. (Previously Presented) The system of claim 1, wherein the plurality of information signals are multiplexed transmission signals selected from the group of frequency divided multiplexed transmission signals, time divided multiplexed transmission signals, code divided multiplexed transmission signals, wavelength divided multiplexed transmission signals, and dense wavelength divided multiplexed transmission signals.

Claims 15-52. (Cancel)